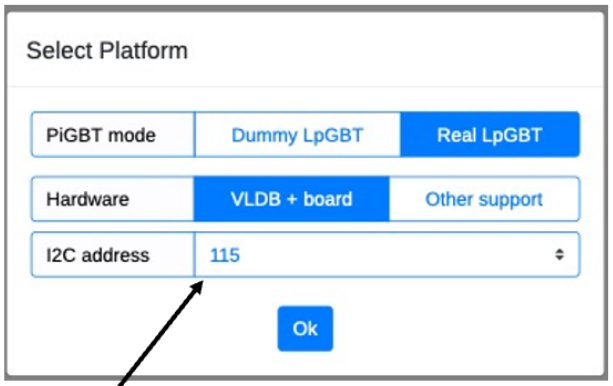
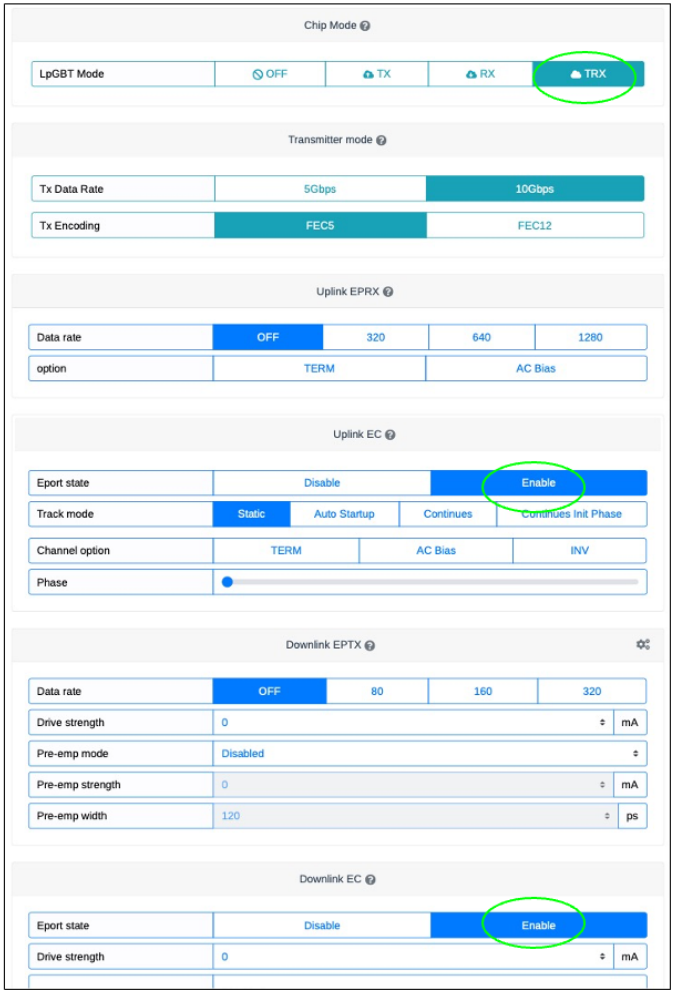
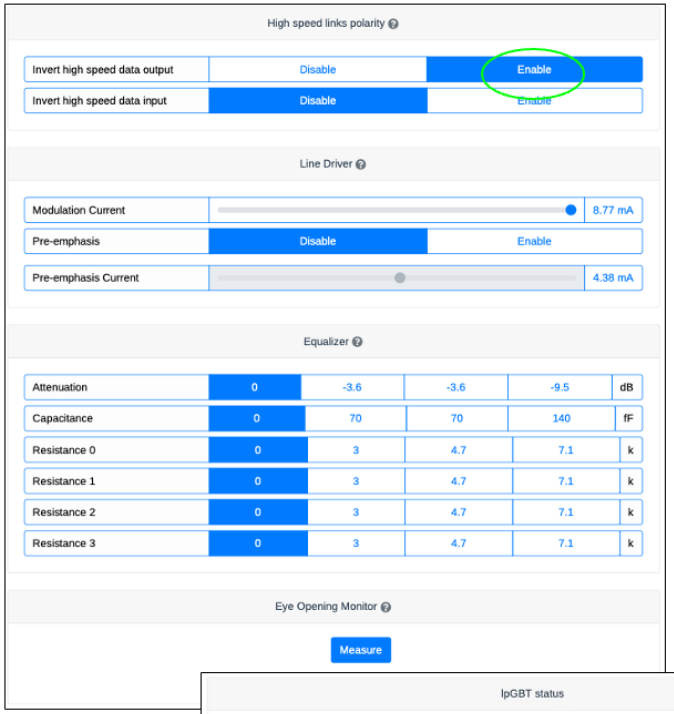
First, select the components of the setup:



The only address available to select

Second, set the lpGBT mode on TRX, enable the uplink and downlink EC channel, click on Quick configuration and send again the configuration to the lpGBT:





Invert the high-speed link polarity for the output (in the High Speed settings)

AND:
in the Registers settings, load the config file, even if there's no AMAC chip on module, to set the clock.